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PATENT  
81788:0163

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Yoshiyuki Tanaka, et al.

Serial No: 09/130,818

Filed: August 7, 1998

For: METHOD FOR CONTROLLING NON-VOLATILE SEMICONDUCTOR MEMORY SYSTEM BY USING LOOK UP TABLE (As Amended)

Art Unit: 2759

Examiner: D. Robertson

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to:	
Commissioner for Patents	
Washington D.C. 20231, on	
March 19, 2001	
Date of Deposit	
William H. Wright, Reg. No. 36,312	
Name	03/19/01
Signature	Date

*Wright*

AMENDMENT

Commissioner for Patents  
Washington, D.C. 20231

Dear Sir:

This is in response to the Office Action dated September 19, 2000, which was paper #8 of the present application. The time for response to which is extended three months by the accompanying petition from December 19, 2000 to March 19, 2001. Please amend the above-referenced application as follows:

IN THE DRAWINGS:

Amendments to the drawings are indicated in red on the attached sheets. Should the Examiner approve this amendments, applicant will submit replacement drawings.

IN THE TITLE:

Please amend the title to read:

--Method for Controlling Non-Volatile Semiconductor Memory System by Using Look up Table--.